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retrieving said class data from addresses of said memory
corresponding to said class of said first digital image signal;
and
generating all of said pixel data, representing pixel values
of said second digital image signal, in the same manner in
accordance with a common algorithm based upon said retrieved
class data.

REMARKS

In light of the amendments to the application noted above and remarks to follow, reconsideration and allowance of the above-referenced application are respectfully requested.

Claims 1-38 are pending. Claims 1-14 are the original patent claims and stand unamended. Claims 15-38 are added claims. Claims 15, 19, 23, 27, 30, 33 and 36 are amended herein. Claims 16-18, 20-22, 24-26, 28-29, 31-32, 34-35 and 37-38 stand unamended. Support for the amendment to claims 15, 19, 23, 27, 30, 33 and 36 including the recitation that the class data is obtained by learning is found in the specification as originally filed and therefore does not constitute new matter. Applicant presents this Preliminary Amendment as a complete response to the Final Office Action dated July 27, 2001 in the parent application.

At paragraph 3 of the July 27, 2002 Final Office Action in the parent application, the Examiner has rejected claims 15, 17,

19, 21, 22, 33, 35-36 and 38 under 35 U.S.C. §103(a) as being unpatentable over Kanno et al. (U.S. Patent No. 5,229,868) in view of Matsumura (U.S. Patent No. 5,148,499). Applicant respectfully traverses the rejection.

*Kanno
Fig. 11
element 2* Applicant has amended each of the pending independent claims to recite that the class data is obtained by learning. Applicant submits that none of the references relied upon by the Examiner depicts this learning feature.

Applicant has amended independent claims 15, 19, 33 and 36 to recite a similar limitation. This therefore precludes the use of any predetermined class data and results in a more accurate class definition. Therefore, because the combination of references relied upon by the Examiner fails to depict this feature as claimed in the independent claims noted above, Applicant respectfully requests that the rejection of independent claims 15, 19, 33 and 36 under 35 U.S.C. §103(a) be withdrawn.

Furthermore, dependent claims 17, 21, 22, 35 and 38 depend, either directly or indirectly, from one of independent claims 15, 19, 33 or 36, and are therefore allowable as depending from allowable independent base claims. Additionally, each of these claims presents an independently patentable combination in and of its own right, and is therefore patentable for this additional reason. Applicant therefore similarly requests that the rejection of claims 17, 21, 22, 35 and 38 under 35 U.S.C. §102(e) be withdrawn.

At paragraph 4 of the outstanding Office Action from the parent application, the Examiner has rejected claims 18, 23, 25, 26, 27, 30 and 32 under 35 U.S.C. §103(a) as being unpatentable over Kanno et al. and Matsumura, and further in view of Collins (U.S. Patent No. 4,587,556). Applicant respectfully traverses the rejection.

Independent claims 23, 27 and 30 have been amended to include limitations similar to those noted above with respect to independent claims 15, 19, 33 and 36, specifically that the class data is obtained by learning. Because Collins fails to cure the defects of Kanno et al. and Matsumura noted above, Applicant submits that independent claims 23, 27 and 30 are allowable over the combination of prior art relied upon by the Examiner, and therefore respectfully requests that the rejection of claims 23, 27 and 30 under 35 U.S.C. §103(a) be withdrawn.

Furthermore, claims 18, 25, 26, 29 and 32 depend, either directly or indirectly from an independent allowable claim, and are therefore allowable as depending from an allowable independent base claim. Additionally, each of these claims depicts an independently patentable combination in and of its own right. For these reasons, Applicant respectfully requests that the rejection of claims 18, 25, 26, 29 and 32 under 35 U.S.C. §103(a) be withdrawn.

At paragraph 5 of the outstanding Office Action in the parent application, the Examiner has rejected claims 16, 20, 34

and 37 under 35 U.S.C. §103(a) as being unpatentable over Kanno et al. and Matsumura and further in view of Tararine et al. (U.S. Patent No. 5,048,102). Applicants respectfully traverse the rejection.

Claims 16, 20, 34 and 37 each depend from one of the independent claims noted above, and therefore is allowable as depending from an allowable independent base claim. Additionally, because Tararine et al. fails to cure the defects noted above with respect to Kanno et al. and Matsumura, Applicant submits that each of these claims also presents an independently patentable combination in and of its own right. Applicant therefore respectfully requests that the rejection of claims 16, 20, 34 and 37 under 35 U.S.C. §103(a) be withdrawn.

Applicant notes with appreciation the notice that claims 1-14 are allowable over the prior art of record. To the extent the Examiner's stated reasons for allowability imply or are construed to mean that the claims are allowable over the prior art of record because the Examiner believes the claims should be interpreted to include one or more features or limitations not recited therein, Applicant's attorney disagrees with such an interpretation. It is the intent of Applicant, by his attorney, to construe the allowed claims so as to cover the invention disclosed in the instant application and all equivalents to which the claimed invention is entitled.

CONCLUSION

Statements appearing above in respect to the disclosures and the cited references represent the present opinion of Applicant's undersigned attorney and, in the event that the Examiner disagrees with any of such opinions, it is respectfully requested that the Examiner specifically indicate those portions of the reference providing a basis for a contrary view.

Applicant has made a diligent effort to place claims 15-38 in condition for allowance, and notice of the allowance of these claims in addition to claims 1-14 is earnestly solicited. If the Examiner is unable to issue a Notice of Allowance regarding these claims, the Examiner is requested to contact the undersigned attorney in order to discuss any further outstanding issues.

Early and favorable consideration is respectfully requested.

Please charge additional fees incurred by reason of this response or credit any overpayment to Deposit Account No. 50-0320.

Respectfully submitted,

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PATENT
450100-2780.1

VERSION WITH MARKINGS SHOWING CHANGES MADE

IN THE CLAIMS:

Claims 15, 19, 23, 27, 30, 33 and 36 have been amended as follows:

15. (Three Times Amended) Digital signal conversion apparatus for converting a first digital image signal to a second digital image signal having a high resolution component, comprising:

a memory for storing class data for respective classes at addresses corresponding to said respective classes, said class data [being associated] obtained by learning with at least a training digital image signal having said high resolution component;

means for receiving said first digital image signal including pixel data representing pixel values;

means for clustering a plurality of pixel data of said first digital image signal adjacent to a pixel data of said second digital image signal to produce a class;

means for retrieving said class data from one of said addresses of said memory corresponding to said class of said first digital image signal; and

means for generating all of pixel data, representing pixel values of said second digital image signal, in the same manner in accordance with a common algorithm based upon at least said retrieved class data.

19. (Three Times Amended) A digital signal data conversion method for converting a first digital image signal to a second digital image signal having a high resolution component, comprising the steps of:

storing class data for respective classes at addresses in a memory corresponding to said respective classes, said class data [being associated] obtained by learning with at least a training digital image signal having said high resolution component;

receiving said first digital image signal including pixel data representing pixel values;

clustering a plurality of pixel data of said first digital image signal adjacent to a pixel data of said second digital image signal to produce a class;

retrieving said class data from one of said addresses of said memory corresponding to said class of said first digital video signal; and

generating all of pixel data, representing pixel values of said second digital image signal, in the same manner in accordance with a common algorithm based upon at least said retrieved class data.

23. (Three Times Amended) Digital signal conversion apparatus for converting a digital video signal admitting of a first standard into a digital video signal admitting of a second standard, a first resolution of said digital video signal

admitting of said first standard being lower than a second resolution of said digital video signal admitting of said second standard, comprising:

a memory for storing class data for respective classes at addresses corresponding to said respective classes, said class data [being associated] obtained by learning with at least a training digital video signal admitting of said second standard having said second resolution;

means for receiving an input digital video signal including pixel data and admitting of said first standard;

means for clustering a plurality of pixel data of said input digital video signal adjacent to a pixel data of a second digital video signal to produce a class;

means for retrieving said class data from one of said addresses of said memory corresponding to said class of said input digital video signal admitting of said first standard; and

means for generating all of pixel data, representing pixel values of said digital video signal admitting of said second standard, in the same manner in accordance with a common algorithm based upon at least said class data which has been retrieved.

27. (Three Times Amended) Digital signal conversion apparatus for converting a standard definition digital video signal to a high definition digital video signal, comprising:

a memory for storing class data for respective classes at addresses corresponding to said respective classes, said class data [being associated] obtained by learning with at least a training high definition video signal;

means for receiving a standard definition digital video signal having pixel data representing pixel values;

means for clustering a plurality of pixel data of said standard definition digital video signal adjacent to a pixel data of a second digital video signal to produce a class;

means for retrieving said class data from one of said addresses of said memory corresponding to said class of said standard definition digital video signal; and

means for generating all of pixel data, representing pixel values of a high definition digital video signal, in the same manner in accordance with a common algorithm based upon at least said retrieved class data.

30. (Three Times Amended) A digital signal conversion method, comprising the steps of:

storing class data for respective classes at addresses in a memory corresponding to said respective classes, said class data [being associated] obtained by learning with at least a training high definition digital video signal;

receiving a standard definition digital video signal having pixel data representing pixel values;

clustering a plurality of pixel data of said standard definition digital video signal adjacent to a pixel data of a second digital video signal to produce a class;
retrieving said stored class data from one of said addresses corresponding to said class of said standard definition digital video signal; and
generating all of pixel data, representing pixel values of a second output digital video signal, in the same manner in accordance with a common algorithm based upon at least said retrieved class data.

33. (Three Times Amended) Digital data conversion apparatus for converting a first digital image signal to a second digital image signal having a high resolution component, comprising:

a memory for storing class data for respective classes at addresses corresponding to said respective classes, said class data [being associated] obtained by learning with at least a training digital image data having said high resolution component;

means for receiving said first digital image signal including pixel data representing pixel values;

means for clustering a plurality of pixel data of said first digital image signal adjacent to a plurality of pixel data of said second digital image signal to produce a class, said class being used to retrieve a class data to generate a plurality of

pixel data representing pixel values of a second digital image signal;

means for retrieving said class data from addresses of said memory corresponding to said class of said first digital image signal; and

means for generating all of said pixel data, representing pixel values of said second digital image signal, in the same manner in accordance with a common algorithm based upon at least said retrieved class data.

36. (Three Times Amended) Digital data conversion method for converting a first digital image signal to a second digital image signal having a high resolution component, comprising the steps of:

storing class data for respective classes at addresses in a memory corresponding to said respective classes, said class data [being associated] obtained by learning with at least a training digital image data having said high resolution component;

receiving said first digital image signal including pixel data representing pixel values;

clustering a plurality of pixel data of said first digital image signal adjacent to a plurality of pixel data of said second digital image signal to produce a class, said class being used to retrieve a class data to generate a plurality of pixel data representing pixel values of a second digital image signal;

retrieving said class data from addresses of said memory
corresponding to said class of said first digital image signal;
and

generating all of said pixel data, representing pixel values
of said second digital image signal, in the same manner in
accordance with a common algorithm based upon said retrieved
class data.

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